C51M/C51D POWER PLANE/GND & BYPASS

PROJECT : AT8
Quanta Computer Inc.
PLACE NEAR GPU

VGA_VDD3
VGA_VDDQ
VGA_VDD
3V
SPDIF_VGA
RB500
and Rc and replace C with 0ohm resistor.

Date: Thursday, December 29, 2005

2ACustom
Channel C is available on G73M only

1.8V

VREF = FBVDDQ * Rbot/(Rtop + Rbot)

PROJECT: AT8
Quanta Computer Inc.

G72M/G73M: STUFF
UMA/NC

Channel C is available on G73M only

1.8V
VDDIO is used in determining which HD Audio bus voltage is present on the system. When VDDIO is +1.5V, the device will use 1.5v signaling on the HDA interface pins; when VDDIO is +3.3v, the device will use 3.3v signaling on the HDA interface pins.
R10 and TXI0 must be placed near pin 6 (RXI) and there should be no vias on the (RXI) net.
Vout = 0.7V (1 + Ra/Rb)

Fix 1.8V Output

Vcs = I_L (A) * L_DCR (mOhm) = V_ILIM (mV) / 10