### RESISTOR

<table>
<thead>
<tr>
<th>Symbol name</th>
<th>Value</th>
<th>Tolerance</th>
<th>Rating</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>10KR3</td>
<td>10K Ohm</td>
<td></td>
<td>1/16W, 75V</td>
<td>0603</td>
</tr>
<tr>
<td>333DR5</td>
<td>33.3 Ohm</td>
<td></td>
<td>1/10W, 100V</td>
<td>0805</td>
</tr>
<tr>
<td>1KR3F</td>
<td>1K Ohm</td>
<td>F: 1%</td>
<td>1/16W, 75V</td>
<td>0603</td>
</tr>
</tbody>
</table>

1. The naming rule is \( R \) + size + tolerance.
2. For the value, it can be read by the number before \( R \). (R means resistor)
3. For the tolerance, it can be read from the last letter.
4. For the rating, we don't show on the symbol name.
5. For the size, \( R2\Rightarrow0402, R3\Rightarrow0603, R5\Rightarrow0805,... \)

### CAPACITOR

<table>
<thead>
<tr>
<th>Symbol name</th>
<th>Value</th>
<th>Tolerance</th>
<th>Rating</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>SC01U10V2MX-1</td>
<td>1uF</td>
<td>XI5XR</td>
<td>10V</td>
<td>0402</td>
</tr>
<tr>
<td>SC106D35V5MX</td>
<td>10uF</td>
<td>XI5XR</td>
<td>6.3V</td>
<td>0805</td>
</tr>
<tr>
<td>SC02U10V52Y</td>
<td>2.2uF</td>
<td>ZY5V</td>
<td>16V</td>
<td>0805</td>
</tr>
</tbody>
</table>

1. The naming rule is \( C \) + type + rating + size + tolerance + material.
2. SC01U10V2MX-1: 1uF SMT Ceramic, XI5XR POS cap or XR cap.
3. SC106D35V5MX: 10uF Tolerance M, K, Z.
4. SC02U10V52Y: 2.2uF Tolerance ZY5V.

### PCI TABLE

<table>
<thead>
<tr>
<th>Device</th>
<th>IDESEL</th>
<th>IRQ (Default)</th>
<th>REQ#/ GNT#</th>
</tr>
</thead>
<tbody>
<tr>
<td>MINIPCISLOT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CARDBUS.RSC811</td>
<td>AD16</td>
<td>SERIRQ</td>
<td>REQ/H GNTH</td>
</tr>
<tr>
<td>USB UHCI</td>
<td>AD29</td>
<td>A, C, D</td>
<td></td>
</tr>
<tr>
<td>USB 2.0 ENCI</td>
<td>AD29</td>
<td>H</td>
<td></td>
</tr>
<tr>
<td>DMI-to-PCI/AC97 Modem/AC97 Audio</td>
<td>AD30</td>
<td>B</td>
<td></td>
</tr>
<tr>
<td>LPC Bridge</td>
<td>AD31</td>
<td>C</td>
<td></td>
</tr>
<tr>
<td>EXPRESS</td>
<td>AD29</td>
<td>A, B, C, D</td>
<td></td>
</tr>
</tbody>
</table>

### EC HISTORY

- SDV: 06/03/2005 EC016
- SDV: 06/07/2005 EC027
- SIV: 06/12/2005 EC001, EC002
- SIV: 06/28/2005 EC003
- SIV: 07/01/2005 EC004
- SIV: 07/07/2005 EC005
- SIV: 07/12/2005 EC006
- SIV: 07/15/2005 EC007
- SIV: 07/18/2005 EC008
- SIV: 07/19/2005 EC009
- SIV: 07/21/2005 EC010
- SDV: 04/13/2005 EC013
- SDV: 04/14/2005 EC014
- SDV: 04/15/2005 EC015
- SDV: 04/19/2005 EC016
- SDV: 04/21/2005 EC017
- SDV: 04/25/2005 EC018
- SDV: 04/27/2005 EC019
- SDV: 04/28/2005 EC020
- SDV: 05/02/2005 EC023
- SDV: 05/13/2005 EC024
- SDV: 05/16/2005 EC025

### PLANAR_ID(3,0)

<table>
<thead>
<tr>
<th>ICH7.M GPIO(I3)</th>
<th>ICH7.M GPIO(I3)</th>
<th>ICH7.M GPIO(I3)</th>
</tr>
</thead>
<tbody>
<tr>
<td>39</td>
<td>38</td>
<td>37</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

**Note:**
- SDV: SYSTEM DESIGNER VERSION
- SIV: SOFTWARE INTERFACE VERSION

**More Details:**
- USB UHCI: Universal Serial Bus Hub Interface
- PCI Express: Peripheral Component Interconnect Express
- PCI Table Entries...

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www.kytkim.com
PLACE ONE 10UF AND 0.01UF NEAR VCCA PIN.
(*1) TCK SIGNAL IS BRANCHED AT DOTHAN PIN

(*2) CPURST# SIGNAL IS BRANCHED AT ALVISO PIN
Place Test PAD Near to Chip as could as possible
NOTE: 0.1UF CAPS USED IN VCCD_LVDS, VCCA, LVDS, VCC_T, LVDS SHOULD BE PLACED WITHIN 200 MILS EDGE

NOTE: 0.1UF CAPS NEED TO BE LOCATED AS EDGE CAPS WITHIN 200 MILS

Route VSSA/CRTDAC gnd from GMCH to decoupling cap ground lead and then connect to the GND plane

NOTE: CAPS USED IN VCCA, TVBG, VCCD, TVDAC, VCCQ, TVDAC SHOULD BE WITHIN 250 MILS OF EDGE
Place one cap to each power pin and as close as possible.
 CHANNEL A PARALLEL TERMINATION

 CHANNEL B PARALLEL TERMINATION
PLACE 1 CAP FOR EVERY 2 BITS TERMINATION TO VCC0R9B.
EXT CRT INTERFACE

GND GUARDING
EACH SIGNAL WIDTH DEPENDS ON ZO (TRACE IMPEDANCE)
SPACING=8 MIL

ZO=50 OHM
ZO=75 OHM

DCCCLK_ID3 12,46
DDCDATA_ID1 12,46
SLICE_VSYNC 46
SLICE_HSYNC 46
AC coupling caps need to be within 250 mils of the driver.
Each NC pin must have test point pad.
NC pin list: Pin 3, 14, 15, 29, 31, 32, 33, 35, Pin 36, 37, 43, 45, 47, 48

Pin 35 and 36 are not NC pin

Place under AD1981HD

<Variant Name>

Title

Size Document Number Rev Date: Sheet

of

Wistron Corporation
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

KS Note SB

AUDIO AD1981HDJSTZ

Date: Friday, July 22, 2005

www.kythuatvitinh.com
ALL PARTS MUST BE PLACED NEAR TO INTERNAL MICROPHONE
APPLY A NEW P/N FOR 3K4R2J

R388  6K8R2J
R405  6K8R2J
R395  3K4R2J

R722  10K2R
R728  20K2R
R718  20K2R

APPLY A NEW P/N FOR 240KR2J-GP

C398 SCD47U16V3ZY
R688  240KR2F-L-GP
C397 SCD47U16V3ZY
R688  240KR2F-L-GP
C396 SCD47U16V3ZY
R687  240KR2F-L-GP

U78 LP3985IM5X-4D7-4GP
A. A_CADDR21: CARDBUS CLK 33MHZ
- ADR, -AIOW: USB2.0
SD Slot
Change to Vidalia
INSTALL 1% 49 OHM RESISTORS WITH INTEL TEKOA 82573M

THESE R AND C SHOULD BE PLACED NEAR PHY CHIP(82573M)
APPLY PART NUMBER FOR EZJ1270GA.

ESD REASON

RJ11/RJ45 CONN

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KS Note SB

RJ11/RJ45 CONN

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Golden Finger for Debug Board
Fingerprint

This trace (VCC3B_FINGER) should be routed by wide pattern for POWER.

COMMON MODE CHOKE COIL: TDK ACM2520U
PLACE THESE RESISTORS AS THE CHOKE COIL ARE ALSO PLACED ON THE SMA PADS OF THESE TWO 0-OHM RESISTORS

WIDE PATTERN (MIN 500MA)
PLACE NEAR USB CONNECTOR

This trace (VCC3B_FINGER) should be routed by wide pattern for POWER.
If PMH4 is implemented, IPD_RESET needs a level shift.
**Thermal Sensor LM75 for DDR module**

*For DDR throttling implementation*

**PLACE DIMM area**

- **H8 I2C Bus 1 ADDRESS : 4BH**
  - **TEST PAD FOR BOARD MFG TEST**

These caps must be placed as close as possible to MAX1989.
Keep these two signals as a pair routing!!
Keep these two signals as a pair routing!!
VOUT = 0.249 (VBAT - 5)

Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

KS Note SB

BATTERY MONITOR

Friday, July 22, 2005
6.5A Peak
5.5A Average

These components should be located near by MAX1977

7A

100mA MAX. each

ILI: M: 5*(38.5 / (38.5+110)) = 1.245
1.245 / 15 = 0.83A

ILI: M: 5*(22.6 / (22.6+110)) = 0.8521V
85 / 15 = 5.6A

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Taipei Hsien 221, Taiwan, R.O.C.
CLOSE TO PIN.17 of U72
FOLLOWING 7 PCS OF 0.47UF AREA ASM FOR VCCCPUCORE TO REDUCE EMI

C526 SCD47U25V3KX
C521 SCD47U25V3KX
C528 SCD47U25V3KX
C527 SCD47U25V3KX
C529 SCD47U25V3KX
C7  SCD47U25V3KX
C8  SCD47U25V3KX
Please add RB501V-40 between CV16 and U23_pin8.
It is useful for "Reverse Voltage Input Test."

Trip(LPMODE#=Low)=3.2A
Reset(LPMODE#=High)=2.0A
MAX8765 INP Resistor : 28KR-Ohm
Current Sense Resistor : 10m-Ohm